



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2815

Examiner: Edward Joseph Wojciechowicz

In Re PATENT APPLICATION of:

Applicant(s): Katsuhiro HISAKA

Serial No.: 10/673,392

Filing Date: September 30, 2003

For: FUSE CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME

Atty. Dkt.: IIZ 138

)
)
) **PETITION FOR**
) **ONE-MONTH**
) **EXTENSION OF**
) **TIME**
)
)
)

April 12, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

04/13/2005 SZEWDIE1 00000146 10673392

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120.00 0P

Sir:

It is respectfully requested that the period for replying to the Examiner's Action dated December 14, 2004, be extended for one month under the provisions of 37 C.F.R. 1.136(a).

The applicable extension fee of \$120.00 pursuant to 37 C.F.R. 1.17(a) is submitted herewith.

Should this remittance be accidentally missing or insufficient, the Commissioner is hereby authorized to charge the fee to our Deposit Account No. 18-0002 and advise us accordingly. A duplicate copy of this paper is enclosed.

Respectfully submitted,

Robert H. Berdo, Jr.
Registration No. 38,075
RABIN & BERDO, PC
Customer No. 23995
Telephone: 202-371-8976
Facsimile: 202-408-0924

April 12, 2005
Date

FEE ENCLOSED: \$120.00
Please charge any further
fee to our Deposit Account
No. 18-0002

RHB:vm